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November 17, 2003

Attorney Docket No.: 07977-024004

Mail Stop Patent Application

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Presented for filing is a new divisional patent application of:

Applicant: ISAMU KOBORI AND MICHIO ARAI

Title:

METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD

OF MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-

FILM TRANSISTOR

The prior application is assigned of record to Semiconductor Energy Laboratory Co., Ltd. and TDK Corporation, both Japan corporations, by virtue of an assignment submitted to the Patent and Trademark Office and recorded on March 28, 1996 at reel/frame 7946/0832.

Enclosed are the following papers, including those required to receive a filing date under 37 CFR §1.53(b):

	<u>Pages</u>
Specification	15
Claims	3
Abstract	1
Declaration	2
Drawing(s)	6

Enclosures:

- Copy of priority document (1 page), which was filed in the parent application. Please enter in this application.
- Copy of notice of recordation of assignment document as received in parent (5 pages). Please enter in this application.
- Preliminary amendment and two sheets of drawings, 12 total pages.
- Information disclosure statement, 1 page. PTO-1449, 2 pages.
- Associate Power of Attorney, 1 page, which was filed in the parent application. Please enter in this application.
- Change of correspondence address, 1 page.
- Serial number card (green).



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Commissioner for Patents November 17, 2003 Page 2

This application is a divisional (and claims the benefit of priority under 35 USC 120) of U.S. application serial no. 10/623,581, filed July 22, 2003, which is a divisional of U.S. application serial no. 09/016,999, filed February 2, 1998, which is a divisional of U.S. application serial no. 08/623,506, filed March 28, 1996, which, under 35 USC 119, claims the benefit of a foreign priority application filed in Japan on March 28, 1995 as serial number 07-096266. The disclosures of the prior applications are considered part of (and are incorporated by reference in) the disclosure of this application.

Basic filing fee	\$770
Total claims in excess of 20 times \$18	\$0
Independent claims in excess of 3 times \$86	\$0
Fee for multiple dependent claims	\$0
Total filing fee:	\$770

A check for the filing fee is enclosed. Please apply any other required fees or any credits to deposit account 06-1050, referencing the attorney docket number shown above.

If this application is found to be incomplete, or if a telephone conference would otherwise be helpful, please call the undersigned at (202) 783-5070.

Kindly acknowledge receipt of this application by returning the enclosed postcard.

Please direct all correspondence to the following:

26171

PTO Customer Number

Respectfully submitted,

John F. Hayden

Reg. No. 37,640

Enclosures

JFH/sxw 40187653.doc

CHANGE OF CORRESPONDENCE ADDRESS Application

Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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Application Number	New Divisional Application	
Filing Date		
First Named Inv ntor	Isamu Kobori et al.	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Attorney Docket Number	07977-024004	

Please change the Correspondence Address for the above-identified application to:						
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A₁	pplicant/Inventor.					
Assignee of record of the entire interest. Certificate under 37 CFR 3.73(b) is enclosed.						
⊠ A	ttorney or agent of record. Registr	ation Nur	mber <u>37,640</u>			
Registered practitioner named in the application transmittal letter in an application without an executed oath or declaration. See 37 CFR 1.33(a)(1). Registration Number 37,640						
Typed or Printed Name	John F. Hayden					
Signature	Il Hand					
Date 11 17	03	Telepl	hone (202) 783-5	070		
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.						

★Total of 1 forms are submitted.

Attorney's Docket No.: 07977-024004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Isamu Kobori et al.

Art Unit :

Unknown

Serial No.: New Divisional Application

Examiner:

Unknown

Filed

: Herewith

Confirmation No.:

Unknown

Title

: METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF

MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM

TRANSISTOR

Attention: Official Draftsman

Commissioner for Patents

P.O. Box 1450

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TRANSMITTAL OF FORMAL DRAWINGS

Please substitute the enclosed six (6) sheets of formal drawings (Figs. 1-5D) for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Reg. No. 37,640

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